IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Gustavson, et al.

Serial No.: 10/671.937

Group Art Unit: 2122

Filing Date: September 29, 2003

Examiner:

Not Yet Assigned

For

METHOD AND STRUCTURE FOR PRODUCING HIGH PERFORMANCE LINEAR ALGEBRA ROUTINES USING PRELOADING OF FLOATING POINT

REGISTERS

Honorable Commissioner for Patents Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.98 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. These references were cited by the Examiner in co-pending Application No. 10/671,933, in an Office Action dated December 19, 2006, and in copending Application No. 10/671,934, in an Office Action dated December 18, 2006. A copy of the listed documents are provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Submitted herewith is the IDS fee of \$180.00.

Please charge any deficiencies in fees and credit any overpayment of fees to Assignee's Deposit Account No. 50-0510.

Respectfully submitted,

Date: 3/30/07

Frederick E. Cooperrider, Esq. Registration No. 36,769

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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)
YOR920030171US1
Applicant(s)
Gustavson, et al.

Group Art Unit

Not Yet Assigned

			U.S	. PATENT I	OCUMENTS				
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE		NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
		US-6,601,080	07-2003	Garg, R	ajat P.				
		US-5,983,230	11-1999	Gilbert e	et al.				
	-	US-7.031,994 04-2006 Lap		Lao et al		+	-		
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					A. (1277)				
			U.S. PATENT	T APPLICA	TION PUBLICATIONS				
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME		CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
			FORE	IGN PATEN	T DOCUMENTS				
	REF	DOCUMENT NUMBER	DATE		COUNTRY		SUBCLASS	Translation YES NO	
			OTHER I	DOCUMEN	rs (Including Auth	or, Title, Date, Per	rtinent Pages, Et	c.)	
Phillip et al., PLAPACK: Parallel Linear Algebra Package Design Overview, 1997, IEEE Proceedings of the ACM/IEEE SC97 Conference.									I/IEEE
	Agarwal et al., A High Performance Algorithm Using Pre-Processing for the Sparse Matrix-Vector Multiplication, 1992, IEEE, pages 32-41.								
EXAMINER					DATE CONSIDERED				
		ial if citation considered, whether oclude copy of this form with next			ce with MPEP Section 609	; Draw line throu	gh citation if not	in conform	ance and

Filing Date

September 29, 2003

Form PTO-A820 (also form PTO-1449)

P09A/REV05

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